

Experimental Study and Characterization on the Thermo-Electro Multiphysics Coupling Failure of GaN HEMTs Under High-Power Microwave Pulse

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Abstract—The harsh electromagnetic environment poses a threat to the reliability and safe operation of Gallium nitride (GaN) radio frequency and microwave high-electron-mobility transistors (HEMTs). In this work, we experimentally investigate the thermo-electro multiphysics coupling failure of GaN HEMTs under high-power microwave (HPM) pulse stress. A transient thermal response test and surface electric field distribution measurement methods of GaN HEMTs under HPM stress were proposed. This study demonstrates that at low power HPM injection, the peak temperature of 51 °C is located at the gate–drain access region near the gate side. As the HPM injection power approaches the destruction threshold level, an obvious hot spot of 64 °C is found however strangely in the gate–source access region at the gate edge, in the vicinity of the gate bond pad, or in other words, the input end of the microwave signals. The damage spot is exactly sitting on the hot spot, and also overlapping

with the electric field concentration observed by the electric field scanning. This means it is the thermo-electro multiphysics coupling but not solely the thermal burnout that causes the device failure under HPM pulse stress. These findings are of great significance for predicting damage location and improving the reliability design against the complex electromagnetic environments.

Index Terms—Electromagnetic, gallium nitride (GaN), high electron mobility transistor (HEMT), high-power microwave (HPM) injection.

I. INTRODUCTION

GALLIUM nitride (GaN) high-electron-mobility transistors (HEMTs) have been widely adopted in RF and microwave applications like power amplifiers, low noise amplifiers (LNA), and microwave transmitters, thanks to their extraordinary properties including high efficiency, high power output, and low loss. With the rapid development of wireless communication, radar, and pulse power electronic techniques, the electromagnetic environment for HEMTs become more and more harsh, which can even trigger fatal failures [1], [2], [3], [4], [5], [6]. For example, high-power microwave (HPM) pulses can be coupled to the RF front-end via antenna or cable. This results in a very high-power capacity that exceeds the rated power level of the GaN amplifiers. Specifically, this case induces heat accumulation and can even trigger thermoelectric breakdown in AlGaIn/GaN HEMTs [7], [8], [9], [10], [11]. Furthermore, in electromagnetic irradiation and electromagnetic interference (EMI) environments, the interference-induced mechanical stress can cause bending and shearing, ultimately resulting in device failure [12], [13].

There are already some works of literature about HPM damage effects on RF and microwave devices. In 1981, James et al. [14] studied the damage effects of GaAs Metal-Semiconductor Field Effect Transistor (MESFETs) under X-band RF spike pulses and concluded that the gate–source short circuit accounts for 60% of the failures. Zhou et al. [8] investigated the relation between the power-to-failure of GaN HEMTs and the HPM pulse duration t_{HPM} that varies

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from 100 to 1000 ns. A thermoelectric failure model was derived and the HPM failure was attributed to serious thermo-mechanical stress in the drain field plate. Chen et al. [7] investigated the degradation behavior of GaN HEMTs under 100 μs HPM pulse duration, and the degradation was found to mainly stem from the electron capture by the traps. Xu et al. [15] successfully extracted the transient thermal response including the maximum channel temperature of gallium arsenide (GaAs) FETs and the maximum input power density of the heat source by a hybrid finite element method (FEM), combining the element-by-element FEM (EBE-FEM) with the preconditioned conjugate gradient (PCG) technique. Li et al. [16] established a circuit model that includes an equivalent physical numerical model of active devices and SPICE models of passive devices. This model describes the nonlinear characteristics of the LNA and provides an empirical formula for predicting the pulsewidth and power that cause LNA damage. As can be seen above, the failure mechanism of GaN HEMTs under HPM pulse stress is still unclear, and the electrical and thermal analysis are usually based on simulation [8], [15], [17]. Plus, the monitoring of the transient thermal response process of the GaN HEMTs under HPM pulses is rarely addressed, which is however vital for HPM applications. Besides, the electric field distribution on the device surface, an important role on analyzing the failure mechanism, is seldom reported. Therefore, advanced thermal-electro multiphysics measurements are urgently needed to unveil the root failure mechanism of GaN HEMTs under HPM pulse stress.

In this study, we experimentally investigated the thermal and electric field distributions of GaN HEMTs under HPM stress by an injection method. The HPM injection system and transient thermo-reflectance imaging (TTI) system were utilized for monitoring the transient junction temperature distribution of GaN HEMT under HPM stress. The electric field probe scanning system was adopted to obtain the surface electric field distribution on the GaN HEMTs surface.

II. DEVICE STRUCTURE AND RF PERFORMANCE

The commercial device under test (DUT) is a 6-GHz, 10-W RF HEMT CGH40010 from Wolfspeed [18] with L_G of 0.5 μm , L_{GS} of 1 μm , L_{GD} of 3.5 μm , total W_G of 6.25 mm, and 10 fingers. The absolute maximum rating of the drain–source voltage V_{DS} is 120 V, I_{GMAX} is 4 mA, I_{DMAX} is 1.5 A, $V_{GS(th)}$ is -3 V, gate quiescent voltage $V_{GS(Q)}$ is -2.7 V, power output is 10 W, and upper frequency is 6 GHz. Fig. 1(a) displays the Schematic of the DUT. A detailed cross-sectional scanning electron microscope (SEM) image of the DUT is illustrated by Fig. 1(c)–(e). Usually, the experimental methods for HPM effects include irradiation and injection methods. The injection experiments are less affected by the environment and suitable for device and component level effect research [19]. In our work, the HPM power was injected to the gate of the GaN HEMTs, from the linear region up to the destruction threshold level, during which the RF characteristics were measured. Fig. 1(b) depicts the gain, output power, and maximum power added efficiency (PAE) during the HPM

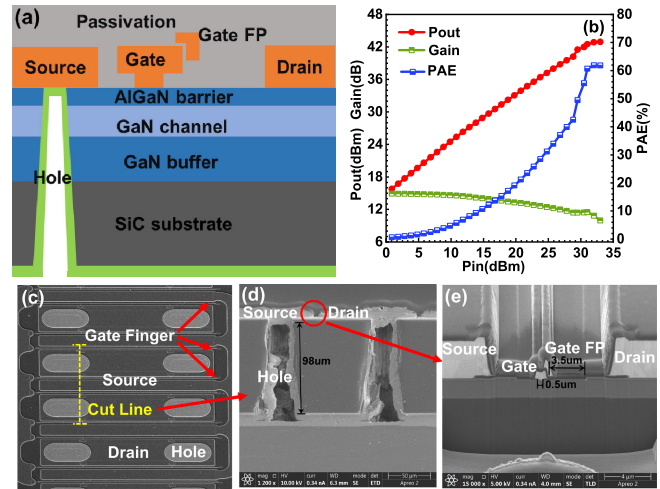


Fig. 1. (a) Schematic cross-sectional view of the GaN HEMTs. (b) RF (radio frequency characteristic) curve of GaN HEMTs during operation. (c) Micrograph. (d) Cross-sectional SEM image along the cutline in (c). (e) Detailed SEM image of the gate area.

injection to the GaN HEMTs. When driven to the 6.9 dB compression point, the device achieved a maximum power output of 43 dBm (20 W) with a maximum PAE of 62%.

III. TRANSIENT THERMAL RESPONSE AND SURFACE ELECTRIC FIELD DISTRIBUTION MEASUREMENT METHODS OF GAN HEMTs UNDER HPM PULSE STRESS

A. Transient Thermal Response Measurement Methods of GaN HEMTs Under HPM Pulse Stress

The infrared thermal imager technique is commonly used for temperature measurements. However, the maximum spatial resolution of this technique is normally over 3 μm , which is not sufficient for GaN RF HEMTs whose gate length is usually below 1 μm [20]. Besides, the maximum temperature in the GaN HEMTs should be located in the 2DEG channel, because the heat is generated by hot electrons collision on the lattice. In order to obtain the accurate transient temperature distribution of GaN HEMTs under HPM stress, an advanced temperature measurement technique named TTI was used in this work, bearing a high spatial resolution of 400 nm and a high temporal resolution of 100 ns. By this, the temperature in the 2DEG channel can be precisely captured with a 365 nm LED light source [21], [22], [23].

The transient thermal response of the GaN HEMTs under HPM stress was tested using a setup consisting of an HPM injection system and a TTI system. The schematic of this setup is shown in Fig. 2(a).

The HPM injection system was made up of a signal generator, a microwave signal source, a solid-state power amplifier, a circulator, a 40-dB directional coupler, a 15-dB attenuator, and a three-channel oscilloscope. The TTI system includes a 2048 \times 2048 pixels CCD camera, an incoherent dispersed low-power UV LED light source with a center wavelength of 365 nm, a 50 \times UV lens, a thermocouple, and a temperature-controlled piezoelectric stage. The principle of

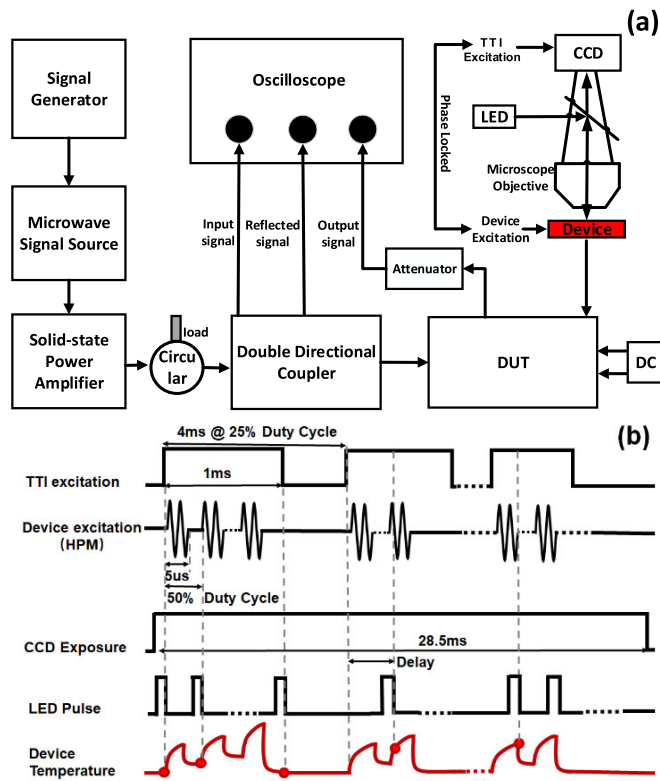


Fig. 2. (a) HPM injection system and TTI (TTI) system. (b) Waveform of various signals under HPM injection and the device temperature transient variation.

thermo-reflectance temperature measurement is based on the change of material reflectivity $\Delta R/R$ with a given temperature rise ΔT , which can be written as follows:

$$\Delta T = \frac{\Delta R}{R \times C_{th}} \quad (1)$$

where the C_{th} is the thermos-reflectance calibration coefficient.

The testing process is performed in two steps. In the first step, the DUT was placed on the temperature-controlled piezoelectric stage and exposed to UV light from the LED to obtain the initial reflectance value R_0 . The C_{th} of the DUT can be derived by the change in the reflected light intensity ΔR with device temperature [24]. The accuracy of this temperature measurement technique is based on how well the C_{th} can be estimated [25]. In this work, the C_{th} of GaN was determined to be $-2.5 \times 10^{-3} \pm 1 \times 10^{-5} \text{ } ^\circ\text{C}^{-1}$, consistent with the report by Pavlidis et al. [21]. In the second step, the signal generator was triggered by TTI to generate a pulse signal, which is used to modulate the solid-state power amplifier to generate a series of HPM pulse signals. The HPM pulses were then injected into the DUT via a circulator and a bi-directional coupler. The temporal signal response was monitored by an oscilloscope. Meanwhile, the TTI system recorded the change in reflected light intensity ΔR of the DUT before and after HPM injection. By using the C_{th} obtained from testing stage I and (1), the transient temperature mapping of the GaN HEMTs under HPM stress can be obtained.

TTI testing is based on lock-in technology to synchronize the excitation signal of the device with the collected signal

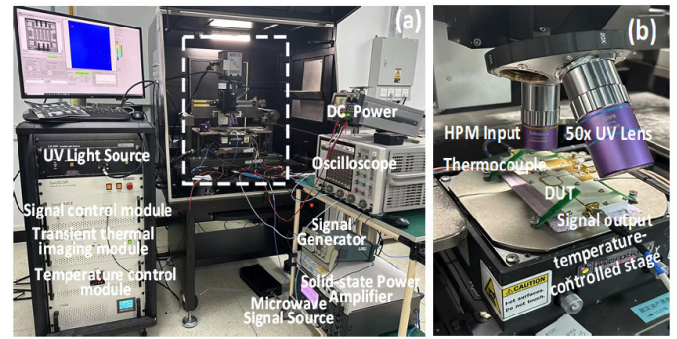


Fig. 3. (a) Photograph of the HPM and TTI system. (b) Details of the dotted box in (a).

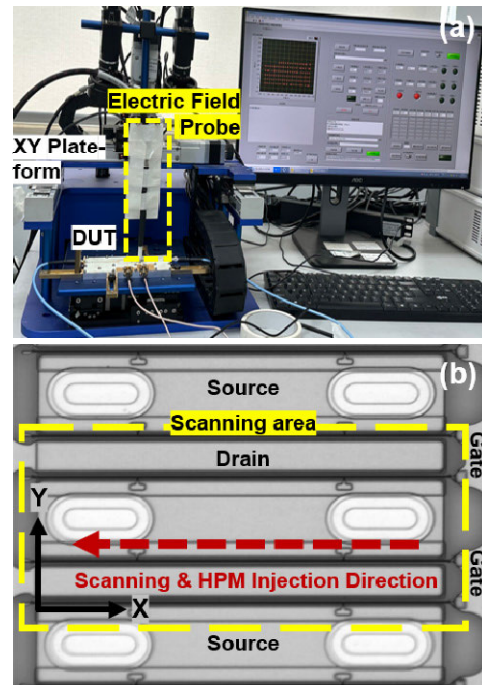


Fig. 4. (a) Image of the setup of the electric field scanning under HPM. (b) HPM signal injection and electric field scanning direction, where the box is the scanning area.

of the detector to obtain a high enough signal-to-noise ratio. Fig. 2(b) illustrates the waveform of these signals under HPM injection and the variation in device temperature. According to current research, the thermal time constant of packaged GaN microwave devices is in the microsecond range [26]. We employed a TTI excitation pulsewidth of 1 ms with a duty cycle of 25%. During the pulsewidth time, the HPM pulse was injected into GaN device with 5 μs pulsewidth and 50% duty cycle. The HPM waveforms are depicted as shown in Fig. 2(b). The frame rate of the transient thermal response system was set to 35 Hz and the CCD exposure time to 28.5 ms. As a result, the system cycles seven times at each CCD exposure time. The temperature distribution of the device within 1 ms is then obtained. During the HPM stress, the quiescent drain bias is 28 V and the gate bias is -2.7 V, and the dc drain current is 200 mA. The photographs of the HPM and TTI systems are shown in Fig. 3.

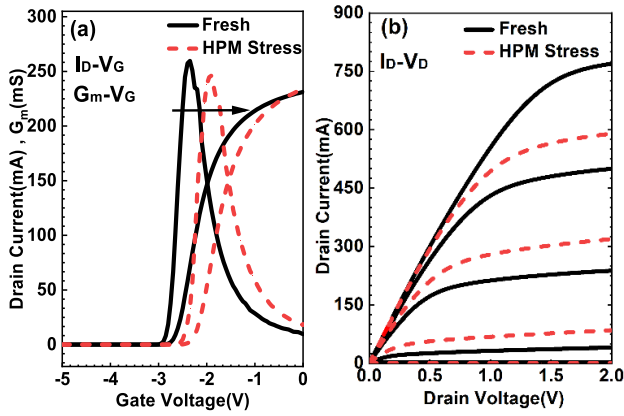


Fig. 5. (a) Transfer and transconductance. (b) Output characteristics of the GaN HEMT before and after the HPM stress.

B. Surface Electric Field Distribution Measurement Methods of GaN HEMTs Under HPM Stress

To further analyze the failure mechanism of GaN HEMT under HPM injection, we utilized LANGER electric field probes to conduct electric field scanning on the surface of the GaN HEMTs under HPM injection. The LANGER probes were fixed on an XY platform, and the DUT was placed directly under the probes [27]. The surface electric field distribution of the GaN HEMT was then scanned by moving the XY platform at micrometer level [28]. The LANGER electric field probes were connected to the spectrum analyzer to display the scanning results. Fig. 4(a) shows the experimental setup. Fig. 4(b) displays the injection direction of the HPM signal and the direction of the electric field scanning.

IV. EXPERIMENTAL RESULTS AND DISCUSSION

Next, we will demonstrate the experimental results and analyze the electrical degradation, transient temperature response, and surface electric field distribution of the GaN HEMT under HPM stress.

A. Electrical Degradation

Fig. 5(a) shows the I_D-V_G and I_D-V_D curves of the device before and after the HPM stress. It can be seen that the HPM injection causes a decrease of the device's saturation drain current and a threshold voltage V_{TH} positive shift by 0.4 V.

In order to investigate the damage effects of HPM injection on GaN HEMT, we measured the gate current I_G during the HPM injection. As can be seen in Fig. 6(a), when the injected power P_{inject} is low (P_{LP}) and the GaN HEMT in the linear to saturation region, the I_G is almost zero. As the injected power approaches the destruction threshold level of P_{TH} , the I_G starts to degrade toward -180 mA. Fig. 6(b) displays the gate leakage current I_{GSS} before and after HPM injection. After P_{LP} stress, a positive shift of the V_{TH} was observed as shown in Fig. 5(a), concomitant with a decrease in I_{GSS} . This phenomenon can be attributed to the capture of channel electrons by surface states under the gate, driven by the gate-drain electric fields. The captured electrons pull up the potential barrier under the gate, thus inducing the gate

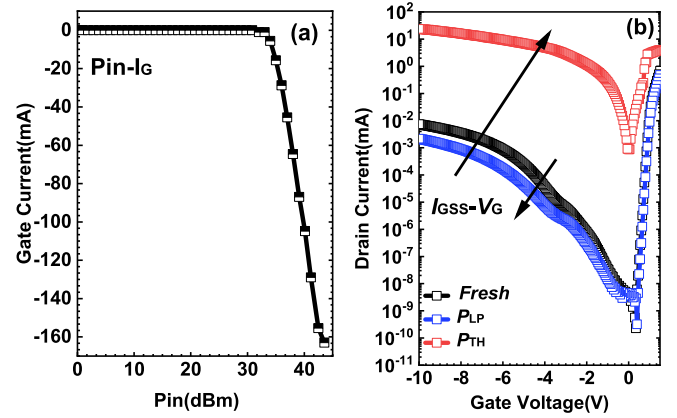


Fig. 6. (a) Gate current versus P_{in} during the HPM injection experiment. (b) Gate current curves of the device in the fresh state, after P_{LP} HPM stress, and close to the P_{TH} HPM stress.

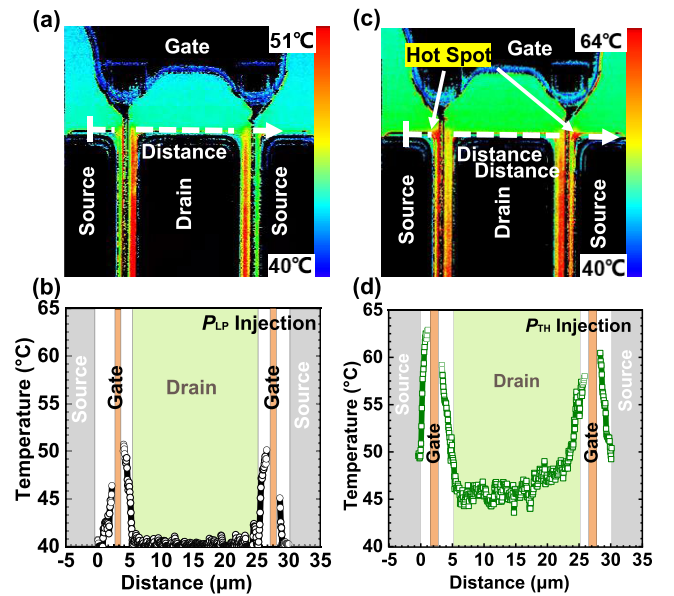


Fig. 7. (a) Temperature distribution of GaN HEMT under P_{LP} stress and (b) its temperature distribution along the horizontal dashed line in (a) from the source to drain and then back to the source. (c) Temperature distribution of the GaN HEMT under P_{TH} stress and (d) its temperature distribution along the horizontal dashed line in (c) from the source to drain and then back to the source.

leakage drop. As the P_{inject} continues to approach the value of P_{TH} , an increase of I_{GSS} by three orders in magnitude was observed, probably stemming from the damage of the Schottky barrier at the gate metal/AlGaN junction.

By changing the pulsewidth and duty cycle of the $P_{injection}$, we obtained the destruction threshold level P_{TH} of the GaN HEMT at different pulsewidth, as shown in the Table I. It can be seen that the P_{TH} decreases with pulsewidth, which is consistent with other HPM injection results [8].

B. Thermal Characteristics

To investigate the failure mechanism of GaN HEMTs under HPM injection, we monitored the thermal distribution during the injection stress. Fig. 7(a) and (c) depicts the temperature distribution of GaN HEMT under P_{LP} and P_{TH} injection, respectively. Fig. 7(b) and (d) displays the

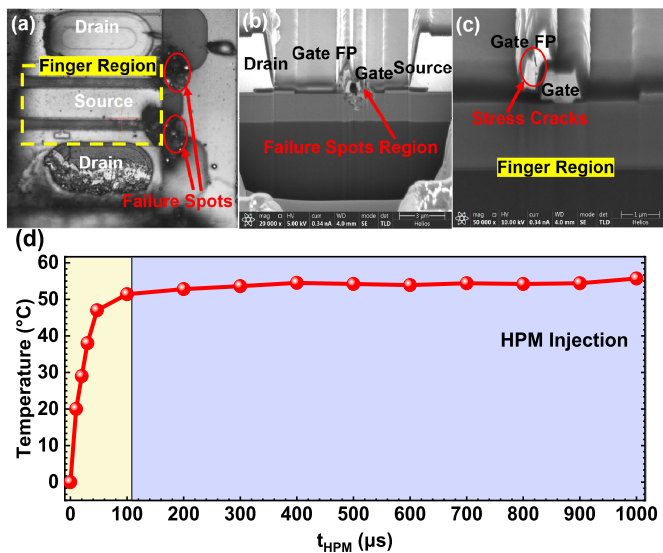


Fig. 8. (a) Micrograph of the GaN HEMT surface after failure under a high-power injection. (b) Cross-sectional SEM image of the gate finger region of the GaN HEMT after failure. (c) Cross-sectional SEM image of the GaN HEMT after burnout. (d) Variation of GaN HEMT channel temperature over time under the 38 W HPM injection.

temperature distribution along the horizontal dashed line from source to drain and then to the source of the HEMTs under the two injection abovementioned, separately. Under P_{LP} injection as shown in Fig. 7(a) and (b), the peak temperature of 51 °C in the GaN channel is located at the gate–drain access region near the gate electrode, which is frequently found in previous reports for GaN HEMTs under stress such as high voltage, self-heating and electrostatic discharge [29], [30]. Under P_{TH} injection as shown in Fig. 7(c) and (d), the increased injection power induces a thermal accumulation near the gate electrode on both the source and drain sides. Furthermore, an obvious hot spot of 64 °C is found in the gate–source access region close to the gate bond pad. This indicates that with the injection power increasing, the peak hot spot in the HEMT shifts from the gate–drain access region to the gate–source access region near the gate pad, which is the microwave signal input port. At this location, high energy loss is caused due to injection power oversaturation.

C. Failure Spots and Analysis

Fig. 8(a) illustrates the surface of the failed GaN HEMT after a high injection power over P_{TH} . The failure spots are located in the vicinity of the gate pad. It should be noted that the failure spots are exactly overlapping with the hot spots found in the transient temperature mappings of Fig. 7(c). Severe damage and even partial melting of the gate electrode is observed by micrograph in Fig. 8(a). The SEM image verifies the failure spot is strangely located in the gate–source access region at the gate edge as shown in Fig. 8(b). Additionally, cracks were observed on the field plate in the finger region, as shown in the SEM image of Fig. 8(c). This phenomenon has also been reported in other studies, which is believed to stem from thermal stress [8].

TABLE I
MEASURED P_{TH} UNDER DIFFERENT HPM PULSEWIDTH

Duty cycle	Pulse Width (μ s)	P_{TH} (W)
20%	2	89
30%	3	56
50%	5	40
100%	10	25

The transient temperature evolution of the GaN HEMT under HPM injection is shown in Fig. 8(d). The channel temperature rises rapidly within the first 0–100 μ s, then gradually saturates after 100 μ s. This saturated temperature stays at a relatively low level, which seems impossible to induce the thermal burnout.

Normally, for the HPM pulses with a very short pulsewidth of <10 ns, the GaN HEMT suffers electrical breakdown and has no time to generate heat; for the pulsewidth >10 ns, electro-thermal coupling failure normally occurs; and for the pulsewidth >100 μ s, one steady state dominates where thermal equilibrium is established, and some studies state that it stems from hot carrier injection. The pulsewidth used in our experiments is 5 μ s with a duty cycle of 50%. Thus, a thermo-electro multiphysics coupling failure instead of thermal burnout is rational to take place in the device.

D. Electric Field Scanning

To verify the hypothesis proposed above, the surface electric field distribution of GaN HEMT under HPM stress is measured. Fig. 9(a) exhibits the electric field distribution on the surface of GaN HEMT in the box area as depicted in Fig. 4(b), under P_{LP} injection. Fig. 9(b) reveals the distribution of the electric field. It can be observed that the electric field at the input end is lower than that at the output end, owing to the amplification of the input signal at the output end.

Fig. 9(c) presents the electric field distribution on the surface of GaN HEMT under P_{TH} injection in the same area, and Fig. 9(d) exhibits the electric field distribution. It can be seen that under P_{TH} injection, the overall surface electric field strength of GaN HEMT is higher than that under P_{LP} injection. It should be noted that the electric field at the signal injection end, i.e., the gate bond pad, is higher than that at the output end in the vicinity of the drain bond pad. This is because, under high-power injection, the injected signals cannot be amplified due to saturation, and most power is reflected back to the input end. A standing wave, located at the corner of the gate bond pad and gate fingers, is thus probably generated due to the large impedance mismatch, leading to a high electric field and failure on the GaN device. This failure mechanism has previously been observed in transmission lines [31]. In view of the relatively low temperature of the hot spot, it is reasonable to confirm that thermal burnout itself, an explanation often used before, cannot easily trigger device failure. Thermo-electro multiphysics coupling failure

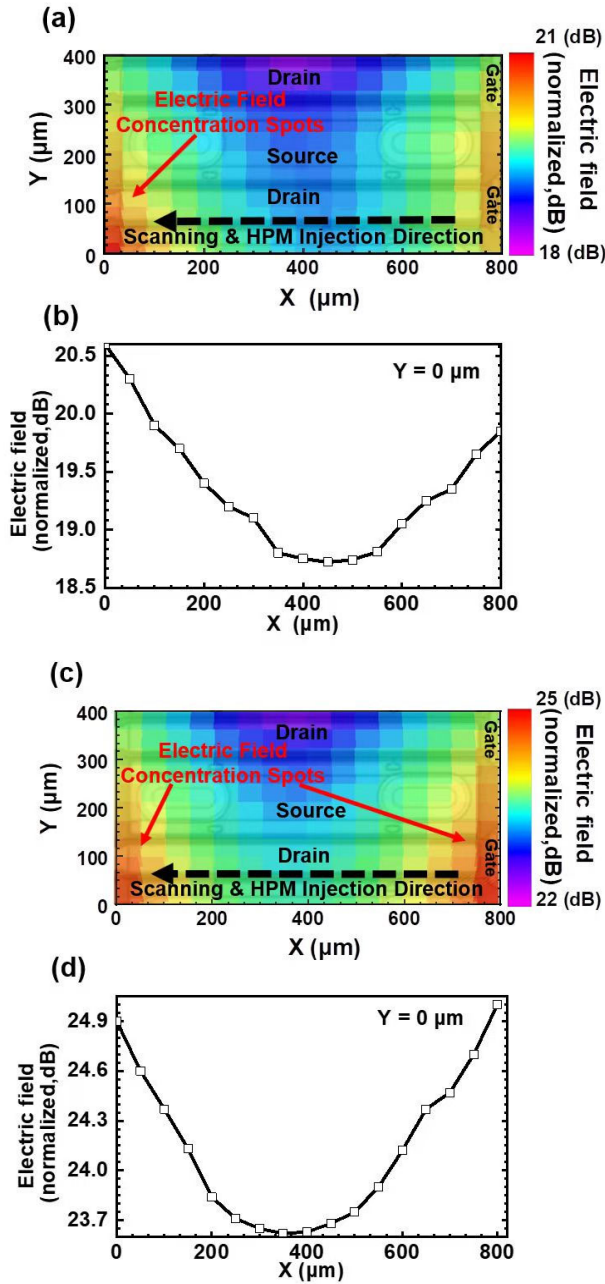


Fig. 9. (a) GaN HEMT surface electric field distribution under P_{LP} HPM stress and (b) its electric field distribution. (c) GaN HEMT surface electric field distribution under P_{TH} HPM stress and (d) its electric field distribution.

of GaN HEMTs under HPM injection is probably the cause of the device failure, as verified by the surface electric field distribution. GaN material, bearing a strong piezoelectric effect, often suffers a severe inverse piezoelectric effect, which is an electric field driven mechanism where the electric field produces mechanical stress [32], [33]. More extreme failure by electrochemical reaction due to the moisture can also take place [34]. This finding on GaN HEMTs under HPM pulse stress unveils a new failure mechanism and provides an idea to improve the reliability of the devices in the harsh electromagnetic environments.

V. CONCLUSION

The failure mechanism of the GaN HEMTs under HPM injection on the gate terminal has been investigated in this work by advanced characterization methods including transient thermal imaging test and surface electric field measurement. It has been found that the location of the hot spot changes with injection power. At low power HPM injection, the hot spot of 51 °C was located at the gate–drain access region near the gate side; whereas at high-power HPM injection, a hot spot of 64 °C was found in the gate–source access region at the gate edge, close to the gate bond pad. Since these peak temperatures themselves are insufficient to trigger thermal burnout, a thermo-electro multiphysics coupling failure mechanism of GaN HEMT under high-power microwave HPM stress was proposed, as verified by the electric field concentration at the failure spot detected by the electric field scanning. This inspiring new finding holds significant meanings for localizing failure spots and protecting GaN HEMTs in complex electromagnetic environments in the future.

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